Power

**Mains power supply**

[Farnell 18VDC 3.33A P1J](https://nl.farnell.com/mean-well/gsm60e18-p1j/adapter-ac-dc-18v-3-33a/dp/3002941)

**Power for FPGA:**

Need to use the power DC jack (2.5 mm Barrel Plug 2.5x5.5) of the DE1SOC, the power cannot be delivered through the GPIO, so we have to feed the power from the pcb to the fpga through the jack see user manual heading 3.6.3 page 26 [1].

**LDO 3.3V:**

[LD29150DT33R](https://nl.farnell.com/stmicroelectronics/ld29150dt33r/ic-reg-ldo-3-3v-1-5a-dpak/dp/1564308)

* 1.5 A output current
* Low Drop out voltage 100mV @ 250mA - 700mV @1.5A

**LDO 5V:**

Will be made by the team members.

# References

|  |  |
| --- | --- |
| [1] | "https://www.mouser.com/datasheet/2/598/DE1-SoC\_User\_manual\_v06-1214842.pdf," [Online]. Available: https://www.mouser.com/datasheet/2/598/DE1-SoC\_User\_manual\_v06-1214842.pdf. |